

Video compression makes big gains

New standards for video compression plus new IC chips will change the world of computing, broadcasting, and communication

Digitally encoded video is upon us. Just as audio compact discs have revolutionized the recording industry, so this new technology promises to unleash a whole slew of video applications—among them, the digital laserdisc, electronic camera, videophone and -conferencing system, image and interactive video tools on personal computers and workstations, program delivery on cable and satellite, and high-definition television (HDTV). Unlike the digital audio technology of the '80s, however, many of the applications of digital video hinge on the use of data compression. The audio bandwidth, after all, is about 20 kHz, which translates into a digital data rate of about 1.4 megabits per second for high-quality stereo sound. Sampled video source signals, on the other hand, require much higher bit rates, ranging from 10 Mb/s for broadcast-quality video to more than 100 Mb/s for HDTV signals.

Even when still pictures are involved, as in image archival systems, a mountain of data is needed to represent them. For example, a color image with resolution of 1000 by 1000 picture elements (pixels) at 24 bits each will occupy 3 megabytes of storage in an uncompressed form. This will not fit onto a high-density floppy diskette, which can hold just 1.2 Mbytes.

Meanwhile, to facilitate industry growth, three standards are being developed for still and moving pictures and for videoconferencing. Sets of chips already exist for all three purposes, some of them proprietary designs, and others in agreement with the standards closest to completion.

HOW IT WORKS. Compression methods build on both redundancies in the data and the nonlinearities of human vision. They exploit correlation in space for still images and in both space and time for video signals. Com-

Peng H. Ang, Peter A. Ruetz, and David Auld
LSI Logic Corp.

pression in space is known as intra-frame compression, while compression in time is called inter-frame compression. Generally, methods that achieve high compression ratios (10:1 to 50:1 for images and 50:1 to 200:1 for video) are lossy in that the reconstructed data are not identical to the original.

Lossless methods do exist, but their compression ratios are far lower, perhaps no better than 3:1. Such techniques are used only in sensitive applications such as medical images. For example, artifacts introduced by a lossy algorithm into an X-ray radiograph may suggest an incorrect interpretation and alter the diagnosis of a medical condition. Conversely, for commercial, industrial and consumer applications, lossy algorithms are preferred because they save on storage and communication bandwidth.

These lossy algorithms also generally exploit aspects of the human visual system. For instance, the eye is much more receptive to fine detail in the luminance (or brightness) signal than in the chrominance (or color) signals. Consequently, the luminance signal is usually sampled at a higher spatial resolution. (For example, in broadcast quality television, the digital resolution of the sampled luminance signal is 720 by 480 pixels, while for the color signals it may be only 360 by 240 pixels.) Second, the encoded (or com-

Video data, even after 100:1 compression, can be decompressed with close to analog videotape quality

pressed) representation of the luminance signal is assigned more bits (a higher dynamic range) than are the chrominance signals.

Also, the eye is less sensitive to energy with high spatial frequency than with low spatial frequency. Indeed, if an image on a 13-inch personal computer monitor were formed by an alternating spatial signal of black and white, the human viewer would see a uniform gray instead of the alternating checkerboard pattern. This deficiency is exploited by coding the high-frequency coefficients with fewer bits and the low ones with more bits.

All these techniques add up to powerful lossy compression algorithms. In many subjective tests, reconstructed images that were encoded with a 20:1 compression ratio are hard to distinguish from the original. Video data, even after compression at ratios at 100:1, can be decompressed with close to analog videotape quality.

STANDARDS. Lack of open standards could slow the growth of this technology and its applications. Three digital video standards that have been proposed are the Joint Photographic Experts Group (JPEG) standard for still picture compression; the Consultative Committee on International Telephony and Telegraphy (CCITT) Recommendation H.261 for video teleconferencing; and the Moving Pictures Experts Group (MPEG) for full-motion compression on digital storage media (DSM).

JPEG's proposed standard is a still-picture-coding algorithm developed by a research team under the auspices of the International Standards Organization (ISO). The team convened in 1987 and the algorithm is currently an ISO committee draft 10918 recommendation. The scope of the algorithm is broad: it comprises a baseline lossy approach and an extended lossless approach, as well as independent functions using coding techniques different from the baseline one. Only the first approach will be discussed here.

The JPEG baseline algorithm falls under the heading of transform-based image coding. A color image can be represented in different color systems. Those in wide use today include R-G-B (the three primary colors red, green, and blue) in the computer industry; Y-U-V (Y for luminance or brightness, U and V for color difference signals Y-R and Y-B, respectively) in the television industry; and C-M-Y-K (cyan, magenta, yellow, and black) in the printing industry.

Within each color system, the constituent parts are called components. Thus, there are three color components in the R-G-B system, four in the C-M-Y-K system.

Each component of the source image in the JPEG encoder and decoder is divided into non-overlapping blocks of 8 by 8 pixels [Fig. 1]. Each block is then transformed using the two-dimensional discrete cosine transform (DCT) with an 8-by-8 kernel.

The resulting 64 coefficients, computed as a 2-D array of 8-by-8 numbers, represent the frequency contents of the given block. The DCT coefficient value in the upper left-

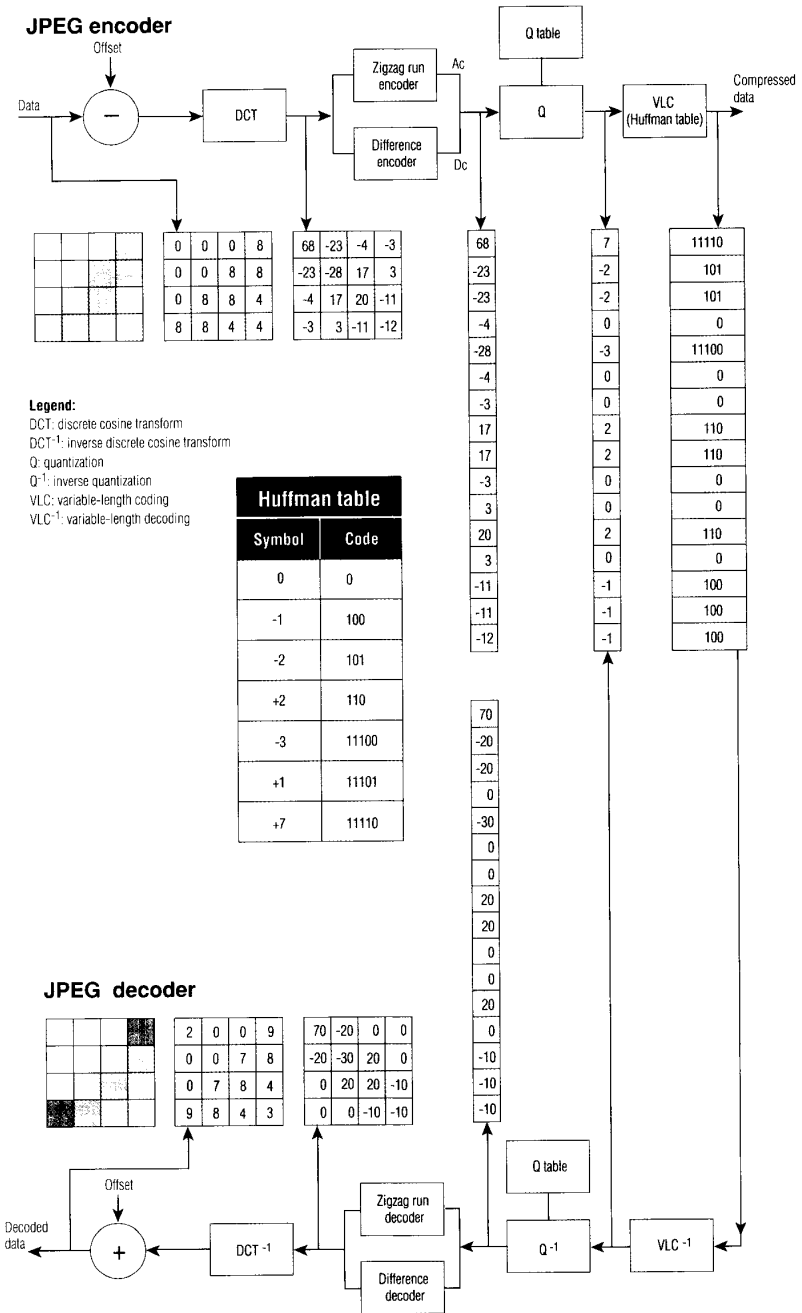
hand corner of the 2-D array measures the energy of the zero-frequency or direct current (dc) term. (For example, if the original 8-by-8 image has a constant value, then only the dc term in the transformed space is non-zero.) The other 63 entries are alternating current (ac) coefficients; they give the relative strengths of signal terms with increasing horizontal frequency from left to right and for terms with increasing vertical frequency from top to bottom.

Next, the DCT coefficients are quantized. The quantization step size varies with frequency and component. The dependence on frequency reflects the fact that the high-frequency coefficients subjectively matter less than the low-frequency ones and may therefore be quantized with a larger step size (that is, more coarsely). In addition, an individual component may have its own quantization table. In the committee draft of the JPEG algorithm, up to four quantization tables are allowed.

Following quantization, the coefficients are re-ordered into a one-dimensional array by reading out the entries of the two-dimensional array along a zigzag route. In this way, the quantized coefficients are “approximately” arranged in order of ascending frequency.

Next, the dc and ac coefficients are loss-

Joint Photographic Experts Group encoder and decoder



[1] The baseline algorithm for the compression of still images included in the Joint Photographic Experts Group (JPEG) proposed standard divides the image into 8-by-8 pixel blocks, represented here by a 4-by-4 block for simplicity. In the encoder, the 16-level gray-scale image is first digitized, then undergoes a discrete cosine transform (DCT) that yields 16 frequency coefficients. The two-dimensional array is read in a zigzag fashion to reorder it into a linear array. The coefficients obtained by quantization—here just dividing by 10—are then coded using the Huffman table, shown here in a simplified version.

The decoding path takes the variable-length coding (VLC) output as input and recovers the quantized coefficients, multiplies each by 10 (inverse quantization) and turns the linear array into a 2-D one through an inverse zigzag operation. An inverse DCT operation subsequently yields a 40-bit image, representing a compression ratio of 1.6:1 here. Much higher ratios can typically be achieved.

not completed its work and the system and audio specifications remain in flux. To a large degree, the functionality of the H.261 encoder block diagram applies. However, the specifics of quantization and motion estimation/compensation and coding are different.

Recently a second phase of standardization dubbed MPEG-2 has begun. Its aim is efficient coding for up to 10 Mb/s with the further goal of higher-quality results.

SILICON SOLUTIONS. Many digital video applications will require low-cost silicon implementations to become broad-based. Toward the end of 1990, there was a flurry of announcements of chip set solutions for image and video compression. Some are nonstandard approaches; others conform to such draft standards as JPEG and H.261.

Solutions for JPEG were offered first by C-Cube Microsystems Inc., San Jose, Calif., then by LSI Logic Corp., Milpitas, Calif. The former is a single-chip CL550 announced in late 1989 and conforms to an early revision of the JPEG draft. The company is currently reworking the design to meet the latest JPEG specifications. LSI Logic's semi-custom two-chip solution combines the L64735 DCT processor with the L64745 quantizer plus JPEG variable-length coder. The partitioning into two chips kept costs down because smaller die have higher yields and can reside in cheaper plastic packages.

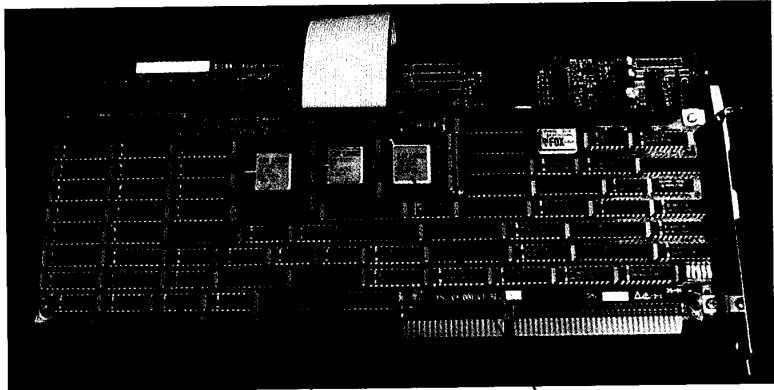
The architectural approaches of C-Cube and LSI Logic are similar. Both the CL550 and the LSI parts implement the JPEG algorithm, but the user may change such JPEG parameters as image resolution, number of components, and the quantization and Huffman code tables used.

H.261 products are available from LSI Logic and Graphics Communications America Ltd. (GCA), Glen Ellyn, Ill. LSI Logic announced a seven-chip building block family for general full-motion video encoding and decoding (including H.261) in September 1990, and GCA followed with a 12-chip building block family about a month later. Both are best classified as function-specific video codec building blocks.

The LSI Logic line consists of four processors and three coding and decoding chips. The processors are the L64720 for motion estimation, the DCT L64730, the L64740 for quantization and L64760 for intra/inter-frame decision. The other three chips are the L64715 Bose-Chaudhuri-Hocquenghen (BCH) error-correcting codec, the L64750 H.261 variable-length encoder and L64751 H.261 variable-length decoder, all specific to H.261.

The four processors may be used in any transform-based compression system. For instance, the MPEG video coding loop (minus the variable-length coder) can be built with the LSI chips.

DESIGN FLEXIBILITY. Function-specific building blocks give video system designers the freedom to configure their own compression architecture at a system level. The parts do



Raster-to-block converter and color-space converter

Discrete cosine transform chip

JPEG quantizer and coder

Image compression boards that comply with the Joint Photographic Experts Group (JPEG) proposed standard are becoming available. The one shown here—Visionary JPEG Compression Engine from Rapid Technology Corp., Amherst, N.Y.—has three LSI Logic Corp. ICs performing raster-to-block conversion, the discrete cosine transform, and quantization and coding. Its connection to Rapid Technology's Visionary Video Board (in the back) allows real-time video data or stills to be directly transferred between the two boards.

not require complicated microcode and are generally easy to use.

In the category of proprietary solutions, there is a two-chip set from Intel Corp., Santa Clara, Calif., and a single-chip from UVC, Irvine, Calif. The two-chip Intel i750 comprises a pixel processor and a display processor, both components of Intel's Digital Video Interactive (DVI) architecture. The pixel processor has a 48-bit-wide microcode instruction and supports the compression and decompression of images and motion video. It also synthesizes graphics primitives. The display processor takes bitmaps from memory and displays them on the CRT. Since May 1990, Intel has been shipping a software product using the chips to perform compression and decompression based on a vector-quantization algorithm achieving real-time performance for 256-by-240-pixel images. A feature of Intel's approach is its programmability.

In its product announcement, UVC claims a chip capable of real-time 500:1 compression, using purely intra-frame techniques.

One other company, SGS-Thomson Microelectronics, Phoenix, Ariz., has announced parts suitable for use in a compression system. It has several DCT processors (STV3200, STV3208, and A121) and a motion estimation processor (STV3220).

A DIGITAL TREND. Many new applications in video technology will be digital, as is underscored by the Federal Communications Commission's recent statement that it favors an all-digital HDTV approach. In the computing arena, interest is already growing in the integration of video with graphics and audio. Often referred to as "multimedia" applications, these will be used in interactive education, next-generation graphics systems, network videoconferencing, and other user-friendly systems.

In short, the technology encompassing digital video and audio integrate the worlds of broadcasting and communication with the world of computing. Ten years from now, these three industries will not be distinguishable.

TO PROBE FURTHER. Two textbooks with good discussions of general image encoding techniques are *Digital Image Processing* by William K. Pratt (John Wiley and Sons, New York, 1991) and *Digital Image Processing* by Rafael C. Gonzalez and Paul Wintz (Addison-Wesley, Reading, Mass., 1986). Arch C. Luther wrote an *IEEE Spectrum* article on interactive digital video (DVI) technology in September 1988, and subsequently authored a definitive book on DVI technology entitled *Digital Video in the PC Environment*, 2nd edition (McGraw-Hill, New York, 1990). Another recent book on HDTV is entitled *HDTV: Advanced Television for the 1990s* by K. Blair Benson and Donald G. Fink (McGraw-Hill, New York, 1991).

The April 1991 issue of the *Communications of the Association for Computing Machinery* (CACM) was devoted to emerging compression standards with relevance to digital multimedia.

ABOUT THE AUTHORS. Peng H. Ang (M) joined LSI Logic Corp., Milpitas, Calif., in July 1984. He is vice president and general manager of the digital signal processing (DSP) business unit, which introduced a line of image and video compression ICs in September 1990.

Peter A. Ruetz (M) joined LSI Logic in June 1986. He is currently a senior member of the DSP group's technical staff.

David Auld joined LSI Logic in 1990. He is currently the applications manager of the DSP group. Prior to joining LSI, he was with Immos Corp., which later became part of SGS-Thomson Microelectronics. ♦